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Application Number	10/608588
Filing Date	6/27/2003
First Named Inventor	POLYAKOV, Evgeny
Art Unit	2664
Examiner Name	MAIS, Mark A.
Attorney Docket Number	TERAD-8-US

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		IEEE 1394 and the Windows Platform: The optimal PC interconnect bus for consumer electronic devices, Dec. 14, 2001.	
		ANONYMOUS: "Reference Manual: Model 2108; VXI Serial Data System Digital Resource Module"--Online-, Feb. 8, 2001, Talon Instruments, XP002274335.	
		ANONYMOUS: "Application Note #2; Model 2108; 1553 Serial Bus Emulation"--Online-, 2001, Talon Instruments, XP002274336.	
		ANONYMOUS: "Reference Manual: Model 2108; TX01/RX01 Interconnect Modules"--Online-, Jan. 24, 2001, Talon Instruments, XP002274337.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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